

Notice of Allowability

Application No.

09/574,647

Examiner

Edwin A. León

Applicant(s)

VAN DOORN, SCHELTO

Art Unit

2833

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Amendment of 9/7/04 and Interview Summary of 10/15/04.
2. ☒ The allowed claim(s) is/are 1-10 and 20-28.
3. ☒ The drawings filed on 18 May 2000 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

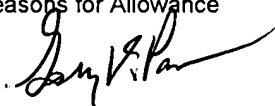
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413),
Paper No./Mail Date 10/15/04.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.


Gary Paumen
Primary Examiner


DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Brenda Binder on October 15, 2004.

The application has been amended as shown in the following attached pages:


Gary Paumen
Primary Examiner

Attorney's Docket No.: 12754-064001 / 00P7629

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Schelto Van Doorn Art Unit : 2833
Serial No. : 09/574,647 Examiner : Edwin A. Leon
Filed : May 18, 2000
Title : ELECTRICALLY CONNECTING INTEGRATED CIRCUITS AND
TRANSDUCERS

PROPOSED EXAMINER'S AMENDMENT

1. (Currently Amended) A transducer comprising:
a housing ~~mountable~~ mounted on a substrate, the housing ~~configured to receive~~ receiving
a jumper cable,
an input/output (I/O) lead supported by the housing ~~and configured to directly contact~~
contacting an I/O lead of an integrated circuit, where the integrated circuit is separately mounted
on the substrate and outside of the housing of the transducer, and
electronic circuitry supported by the housing ~~to transition~~ transitioning between an
electronic data transfer protocol of the jumper cable and an electronic data transfer protocol of
the integrated circuit.
2. (Original) The transducer of claim 1, wherein the transducer I/O lead is configured to
electrically connect to the integrated circuit I/O lead independently of any electrically conductive
path of the substrate.
3. (Original) The transducer of claim 1, wherein the transducer I/O lead is configured to
contact the integrated circuit I/O lead at a transducer surface substantially parallel to a mounting
surface of the substrate.
4. (Original) The transducer of claim 1, wherein the transducer I/O lead is configured to
contact a pin I/O lead of the integrated circuit.
5. (Original) The transducer of claim 1, wherein the transducer I/O lead is configured to
contact a solder ball lead of the integrated circuit.

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6. (Original) The transducer of claim 1, wherein the transducer I/O lead is configured to contact the integrated circuit I/O lead at a transducer surface adjacent to a mounting surface of the substrate.
7. (Original) The transducer of claim 1, further comprising a power input lead connectable to a power line of the substrate.
8. (Original) The transducer of claim 1, further comprising a transductional device.
9. (Original) The transducer of claim 1, wherein the transductional device is an opto-electronic device.
10. (Original) The transducer of claim 1, wherein the transductional device is an electronic device.
- 11 - 19. Cancelled.
20. (Currently Amended) A transducer comprising:
 - a housing ~~mountable~~ mounted on a substrate, the housing ~~configured to receive~~ receiving a jumper cable;
 - an input/output (I/O) lead supported by the housing and ~~configured to directly contact~~ contacting an I/O lead of an integrated circuit, where the integrated circuit is separately mounted on the substrate and outside of the housing of the transducer; and
 - electronic circuitry supported by the housing ~~to transition~~ transitioning between an electronic data transfer protocol of the jumper cable and an electronic data transfer protocol of the integrated circuit, the electronic circuitry including:
 - a coupler ~~operable to couple~~ coupling data signals carried by a jumper cable received by the housing to a transductional device;
 - a transductional device ~~operable to transition~~ transitioning between an electronic data transfer protocol of the jumper cable and an electronic data transfer protocol of the

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integrated circuit; and

a circuit card operable to carry carrying signals between the transductional device and the I/O lead supported by the housing.

21. (New) The transducer of claim 20, wherein the transducer I/O lead is configured to electrically connect to the integrated circuit I/O lead independently of any electrically conductive path of the substrate.
22. (New) The transducer of claim 20, wherein the transducer I/O lead is configured to contact the integrated circuit I/O lead at a transducer surface substantially parallel to a mounting surface of the substrate.
23. (New) The transducer of claim 20, wherein the transducer I/O lead is configured to contact a pin I/O lead of the integrated circuit.
24. (New) The transducer of claim 20, wherein the transducer I/O lead is configured to contact a solder ball lead of the integrated circuit.
25. (New) The transducer of claim 20, wherein the transducer I/O lead is configured to contact the integrated circuit I/O lead at a transducer surface adjacent to a mounting surface of the substrate.
26. (New) The transducer of claim 20, further comprising a power input lead connectable to a power line of the substrate.
27. (New) The transducer of claim 20, wherein the transductional device is an opto-electronic device.
28. (New) The transducer of claim 20, wherein the transductional device is an electronic device.

Allowable Subject Matter

2. Claims 1-10 and 20-28 are allowed.

The references fail to teach, disclose, or suggest, either alone or in combination, the input/output (I/O) lead contacting the I/O lead of the integrated circuit, where the integrated circuit is separately mounted on the substrate and outside of the housing of the transducer and a circuit card carrying signals between the transductional device and the I/O lead supported by the housing and in combination with the rest of the limitations of the base claims.

Applicant's arguments (Pages 5-7) regarding the Curzio reference have been fully considered and are deemed persuasive.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edwin A. León whose telephone number is (571) 272-2008. The examiner can normally be reached on Monday - Friday 10:00-6:30.

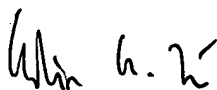
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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula A. Bradley can be reached on 571-272-2800, extension 33. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Gary Pauman
Primary Examiner



Edwin A. Leon
AU 2833

EAL
October 21, 2004